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MITANI et al..

FILING DATE

February 28, 2002

GROUP

2818

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02/28/02

## U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date
DN	AA	6,063,686	5/00	Masuda et al.	438	406
DN	AB	6,043,536	3/00	Numata et al.	257	347
	AC					
	AD					
	AE					
	AF					
	AG					
	AH					

## FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation /Abstract	
						Yes	No
DN	AI	7131025	1995	JAPAN			x
	AJ						
	AK						
	AL						
	AM						

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AN	1997 IEEE International Solid State Circuit Conference, Digest of Technical Papers, 68-69 TP 4.3 [A 1V 46ns 16Mb SOI-DRAM with Body Control Technique]
AO	
AP	
Examiner	<i>Sam R. [Signature]</i>
Date Considered	3/19/03